Electronic Acknowledgement Receipt					
EFS ID:	5371135				
Application Number:	09769953				
International Application Number:					
Confirmation Number:	5912				
Title of Invention:	COMPUTER SYSTEM HAVING CONFIGURABLE CORE LOGIC CHIPSET FOR CONNECTION TO A FAULT-TOLERANT ACCELERATED GRAPHICS PORT BUS AND PERIPHERAL COMPONENT INTERCONNECT BUS				
First Named Inventor/Applicant Name:	Sompong P. Olarig				
Correspondence Address:	INTELLECTUAL PROPERTY ADIMISTRATION				
Filer:	William Patrick O Meara/Deana Ferber				
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Application Type:	Utility under 35 USC 111(a)				
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no

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1 Request for Certificate of Correction	200304082-1.pdf	31573	no	2	
		d9ce5682efb108db385e70248f1b08d534e 4c6b5			

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National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.